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**Conference on Design and Architectures
for Signal and Image Processing**

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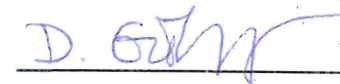

*Evaluation of Analog and Digital Signal Processing on PSoC
Architecture with DCT as Use Case*

Stephan WERNER, Bernhard STIEHLE and Jürgen BECKER





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