7th IEEE International Symposium on Industrial Embedded Systems (SIES) 2012

Best Work-in-Progress (WIP) Paper Award

LISTPARC: Using an Architecture Description Language Approach for Modelling an Adaptive Processor Microarchitecture

Carsten Tradowsky, Florian Thoma, Michael Hübner and Jürgen Becker

Jian-Jia Chen
SIES 2012 General Co-Chair
Karlsruhe, June 20-22, 2012

David Macii
SIES 2012 WIP Co-Chair
Karlsruhe, June 20-22, 2012

Lars Bauer
SIES 2012 WIP Co-Chair
Karlsruhe, June 20-22, 2012