

Tutorials

- *Time Sensitive Networks for Industry 4.0*
- *Propelling Breakthrough Embedded Microprocessors by Means of Integrated Photonics*
- *Low Power Circuit Optimization for IoT*
- *FDSOI Design Experience and Recommendations*
- *Reliability for Industrial and Automotive markets*
- *IBM Q - Introduction into Quantum Computing (with live demo)*
- *Design Automation for Labs-on-Chip: A New Playground for SoC Designers*
- ... and more

Special Sessions

- *Where Machine Learning Meets Design Automation*
- *Secure Multi-Processors Systems-on-Chip for Critical Applications*

Organizing Committee

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Industrial Liaison

- Karan Bhatia - Texas Instruments
- Ram Krishnamurthy - Intel



Early registration cut-off date: August 1 !

Register here: <http://www.ieee-socc.org/registration/registration>

From the Internet of Everything to the Cloud: New drivers for SoC Solutions

For 30 years the IEEE International System-on-Chip Conference (SOCC) has been the premier forum for sharing the latest advancements in SoC architecture, systems, circuit design, process technology, test, design tools, and applications. We proudly continue this tradition with our 30th anniversary conference in Munich, Germany. SOCC 2017 offers a four-day technical program covering a broad range of topics related to SoC research and solutions, from circuit design to new methodologies and future technologies like integrated photonics and quantum computing.

Keynote, plenary and banquet speakers from academic and industry leaders

- * **Gerd Teepe**, Director, Design Enablement, Globalfoundries
“22 and 12 nm FDSOI vs. FINFET for SoC development”
- * **Ron Martino**, Vice President, i.MX Application Processor Product Line, NXP
“Quad core SoC in FDSOI technology”
- * **Josef Hausner**, Division VP R&D IP Strategy, Intel Mobile Communications
“The Path to Global Connectivity – Wireless Communication enters the Next Generation”
- * **Norbert Wehn**, Chair, Microelectronic System Design, University of Kaiserslautern
“The Memory Challenge in Computing Systems: a Survey”

...and more.

Tutorial Day with 8 sessions covering various hot SoC topics and future trends

High-profile Panel Discussion session

Two Special Sessions on Machine Learning and Secure MPSoC

Two-and-a-half days technical sessions with presentations of research and innovation results covering areas in

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| • Memories | • Analog and RF Circuits |
| • Application-Specific Designs, | • Machine Learning and Parallel Architectures |
| • A/D Converters and Low Noise Amplifiers | • Networks-on-Chip, |
| • Design Methodologies for SoCs | • Algorithms, Models, and Simulation for Systems |
| • Design of Reconfigurable and Multiprocessor Systems | • Low-Power Design |
| • Power Management and Harvesting | • On-Chip Fabrics |
| | ...and more |

Bavarian Style Social Events

- * **Anniversary Banquet** at a panoramic mountaintop restaurant in the Alps
- * **Networking Reception**, Poster Session, Lunch, etc.

Accommodation

SOCC has reserved a number of guest rooms at the conference hotel, Novotel Munich City at the conference rate of EUR 129 per night including breakfast. Book early!*

Booking Details: <https://www.ieee-socc.org/venue/conference-venue#accommodation>

*Discounted room block guaranteed until July 23, afterwards limited number of discounted rooms based on availability.

Rooms can be cancelled free of charge until four weeks before arrival.