



7th IEEE International Symposium on Industrial Embedded Systems (SIES) 2012

Best Work-in-Progress (WIP) Paper Award

**LISPARC: Using an Architecture Description Language Approach
for Modelling an Adaptive Processor Microarchitecture**

**Carsten Tradowsky, Florian Thoma,
Michael Hübner and Jürgen Becker**

Jian-Jia Chen
SIES 2012 General Co-Chair
Karlsruhe, June 20-22, 2012

David Macii
SIES 2012 WIP Co-Chair
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Lars Bauer
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